

μ**PD166019T1F**

Data Sheet

Single P-Channel High-Side Intelligent Power Device

R07DS0730EJ0100 Rev.1.00 Apr 25, 2012

Description

The μ PD166019 device is a P-channel high-side switch with diagnostic feedback and embedded protection functions. Due to the adoption of P-channel output switch this device dose not contains charge pump circuit and switching time is controllable by external resistance to IN pin.

Features

- Low noise by no built-in charge pump
- Low on-state resistance: $13.5 \text{ m}\Omega$
- Short circuit protection

— Shutdown by short-circuit detection

- Over temperature protection

 Shutdown with auto-restart on cooling
- Loss of GND protection
- Small multi-chip package: JEDEC 5-pin TO-252
- Built-in Diagnostic function

- Defined fault signal in case of thermal shutdown and/or short circuit shutdown via IN pin.

Ordering Information

Part No.	Lead Plating	Packing	Package
μPD166019T1F-E1-AY ^{*1}	Sn	Tape 2500 p/reel	5-pin TO-252 (MP-3ZK)

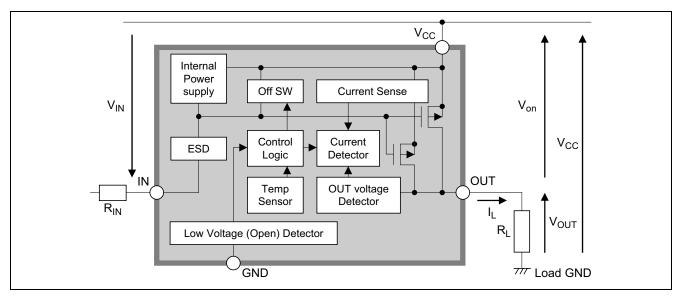
Note: *1 Pb-free (This product does not contain Pb in the external electrode.)

Application

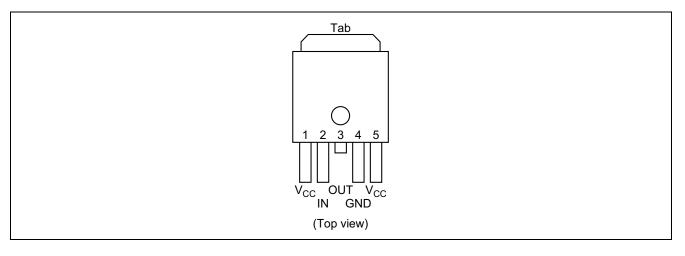
- Light bulb (to 65 W) switching
- Switching of all types of 14 V DC grounded load, such as inductor, resistor and capacitor
- Replacement of fuse and relay
- Note: The information contained in this document is the one that was obtained when the document was issued, and may be subject to change.



Block Diagram



Pin Arrangement



Pin Function

Pin No.	Pin Name	Function
1	V _{CC}	Supply voltage: pin1 and 5 must be externally connected
2	IN	Input: activate the power switch by direct drive of output MOSFET
3/Tab	OUT	Output to load: Tab and pin 3 are internally connected
4	GND	Ground pin Note
5	V _{CC}	Supply voltage: pin1 and 5 must be externally connected

Note: In case of necessity to eliminate the destruction by the soldering bridge to V_{CC}: Pin#5, insert a resistor R_{GND} (recommended 1 k Ω max) between GND pin and system ground. In this condition, current flaws out to IN pin. R_{IN} is recommended 2 k Ω min.

GND to IN current = (R_{GND} \times 1 mA - 0.6 V) / R_{IN}



Absolute Maximum Ratings

				$(Ta = 25^{\circ}C, unless otherwise specified)$
Item	Symbol	Rating	Unit	Test Conditions
V _{CC} voltage	V _{CC1}	28	V	
V _{CC} voltage for full short circuit protection	V _{CC2}	18	V	
V _{CC} voltage (Load Dump)	V _{CC3}	40	V	$\label{eq:R1} \begin{array}{l} R_1 = 1 \ \Omega, \ R_L = 1.5 \ \Omega, \ t_d = 400 \ \text{ms}, \\ IN = low \ or \ high \end{array}$
Load current (short circuit current)	I _{L(SC)}	Self limited	A	
Power Dissipation	PD	59	W	Tc = 25°C
Avalanche current	I _{AS}	34	Α	L = 100 μH
Channel temperature	T _{ch}	-40 to +150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	
Electric discharge capability	V _{ESD}	±2.0	kV	AEC-Q100-022 std
(Human Body Model)				R = 1.5 kΩ, C = 100 pF
Input voltage	VIN	0 to 28	V	IN pin, $V_{IN} \leq V_{CC}$, V_{CC} reference, 1 min
Input current	l _{IN}	-1.0	mA	IN pin

Recommended Operating Conditions

Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Power supply voltage	V _{cc}	7		16	V	$T_{ch} = -40$ to 150°C

Thermal Characteristics

ltem	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Thermal resistance	R _{th(ch-a)}	_	45	55	°C/W	Device on 50 mm \times 50 mm \times 1.5 mm epoxy PCB FR4 with 6 cm 2 of 70 μm copper area



Electrical Characteristics

					$(V_{CC} =$	12 V, $T_{ch} = 25^{\circ}C$, unle	ess otherwise specified)
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions	
Low level input current	IIL	—	—	20	μA	$V_{IN} = 12 \text{ V}, \text{ T}_{ch} = -40$) to 150°C
Input voltage for turn-on	V _{INon}	4.5	—		V	$V_{ON} = 0.16 \text{ V}, I_L = 7.8$	5 A, V _{CC} reference
Input voltage for turn-off	V _{INoff}	—	_	1.5	V	$I_L \le 1 \text{ mA}, V_{CC}$ refere	nce
Standby Current		—	_	1.0	μA	$T_{ch} = 25^{\circ}C$	$V_{IN} = 0 V$
		—	_	1.0	μA	$T_{ch} = -40$ to 150°C	
Output leakage current	I _{OUToff}	—	_	1.0	μA	$T_{ch} = 25^{\circ}C$	$V_{IN} = 0 V$
		—	—	15	μA	$T_{ch} = -40$ to 150°C	
Circuit current	I _{GNDon}	—	_	1.0	mA	$T_{ch} = 25^{\circ}C$	V _{IN} = 12 V
		—	_	1.5	mA	$T_{ch} = -40$ to 150°C	
On state resistance	R _{ON}	—	10.5	13.5	mΩ	$T_{ch} = 25^{\circ}C$	$V_{IN} = 12 \text{ V}, I_L = 7.5 \text{ A}$
		—	17.5	23.0	mΩ	$T_{ch} = 150^{\circ}C$	
On state resistance	R _{ON2}	—	13.5	21.0	mΩ	$T_{ch} = 25^{\circ}C$	$V_{IN} = 4.5 V, I_L = 7.5 A$
		—	23.0	35.0	mΩ	$T_{ch} = 150^{\circ}C$	
Turn on delay time	t _{don}	—	2.2	10	μS	$R_L = 2.2 \Omega, R_{IN} = 0 \Omega$	2,
Turn off delay time	t _{doff}	—	11.3	50	μS	$T_{ch} = -40$ to $150^{\circ}C$	
Rise time	tr	—	4	20	μS]	
Fall time	t _f	_	5	20	μS		

Protection Function

					$(V_{CC} =$	12 V, $T_{ch} = 25^{\circ}C$,	unless otherwise specified)
Item	Symbol	MIN.	TYP.	MAX.	Unit	Tes	t Conditions
Short circuit detection	I _{L7,3(SC)} *1	—	96	135	Α	$T_{ch} = -40^{\circ}C$	$V_{CC} = 7~V,~V_{on} \leq 3~V$
current		33	67	—		$T_{ch} = -25C$	
		19	50	—		T _{ch} = 150°C	
	I _{L9,3(SC)} *1	—	105	145		$T_{ch} = -40^{\circ}C$	$V_{CC} = 9~V,~V_{on} \leq 3~V$
		42	96	-		$T_{ch} = -25C$	
		30	73	—		$T_{ch} = 150^{\circ}C$	
	I _{L12,3(SC)} *1	_	143	190		$T_{ch} = -40^{\circ}C$	$V_{CC} = 12 \ V, \ V_{on} \leq 3 \ V$
		56	130	—		$T_{ch} = -25C$	
		42	105	—		$T_{ch} = 150^{\circ}C$	
	I _{L16,3(SC)}	_	144	190		$T_{ch} = -40^{\circ}C$	$V_{CC} = 16 \ V, \ V_{on} \leq 3 \ V$
		75	131	—		$T_{ch} = -25C$	
		60	107	—		$T_{ch} = 150^{\circ}C$	
Thermal shutdown	T _{th}	150	175	200	°C		
temperature							
Thermal hysteresis	ΔT_{th}	—	14	_	°C	Resume temp: ov	/er 130°C
Under voltage shutdown	V _{CIN(UV)}	3.7	4.8	5.7	V		
Under voltage restart	V _{CIN(ST)}		5.0	6.0	V		
Output current in fault condition	I _{OL}	180	300	—		$V_{IN} = 7 V, V_{CC} = 7$	V V, T _{ch} = -40 to 150°C

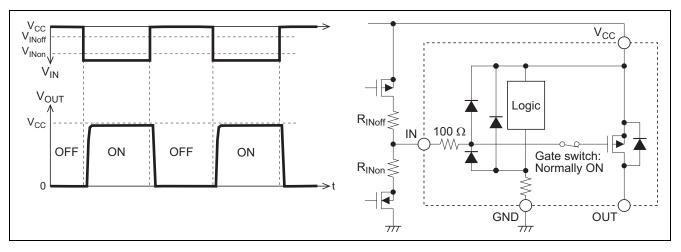
Note: *1 Not subject to production test, specified by design.

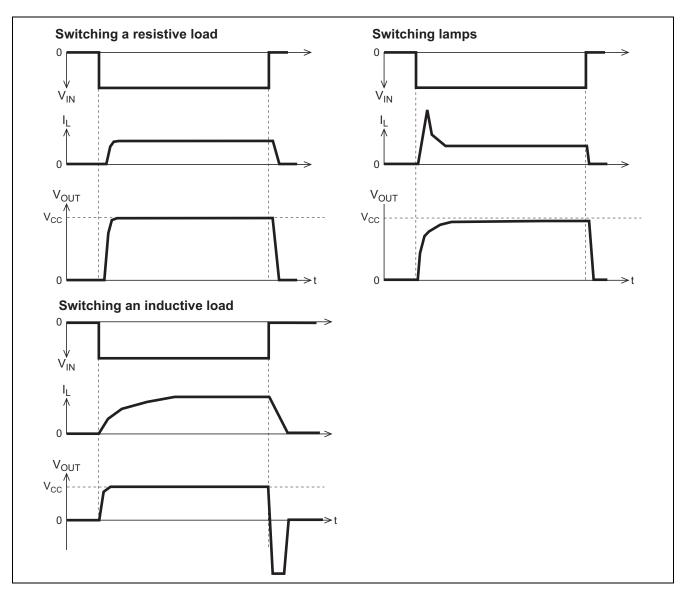


Function Description

Driver Circuit (On-Off Control)

The high-side output, Pch MOSFET is turned on, if the IN input voltage is higher than V_{INon} . The high-side output, Pch MOSFET is turned off, if the IN input voltage is below V_{INoff} . Switching characteristics is adjustable by external resistor to IN pin.





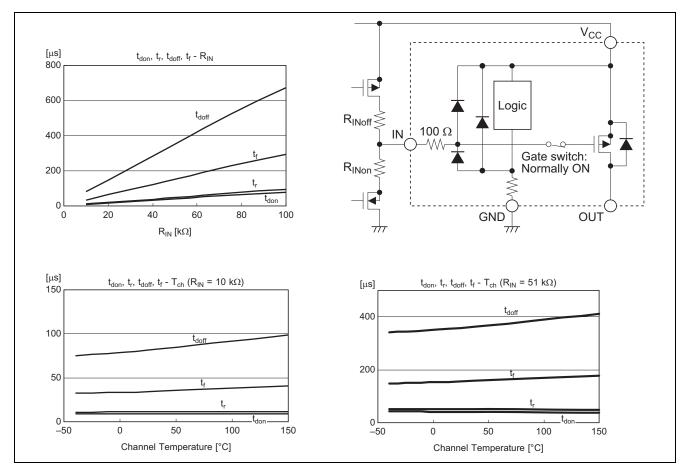


Avalanche Operation at Inductive Load Switch Off

The output MOS, Pch MOSFET is in avalanche when the inductive load is switched off. When the over-voltage is applied to V_{CC} , the output MOS, Pch MOSFET also works in avalanche operation.

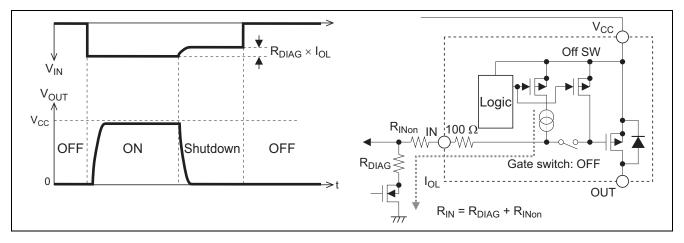
Adjustable Switching Characteristics by External Resistor

Switching characteristics is adjustable by the value of input resistance as following.



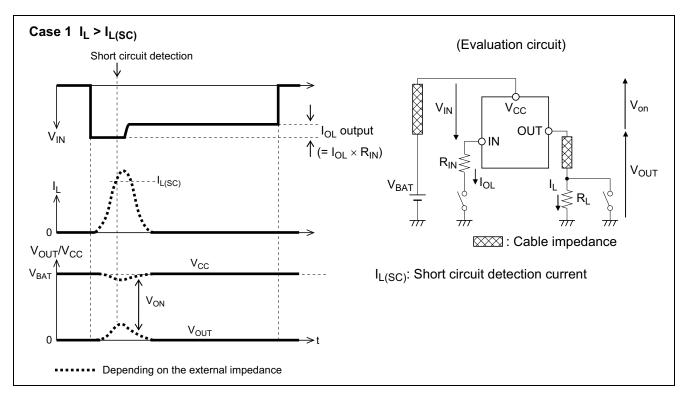
Diagnosis Output via IN Pin

The I_{OL} current starts flawing via IN pin, if the shut down occurred by the protection of short circuit or over temperature. In this condition, internal gate switch is turned-off and the I_{OL} current switch is turned-on. This diagnosis signals is detectable by external circuit, resistor and comparator.

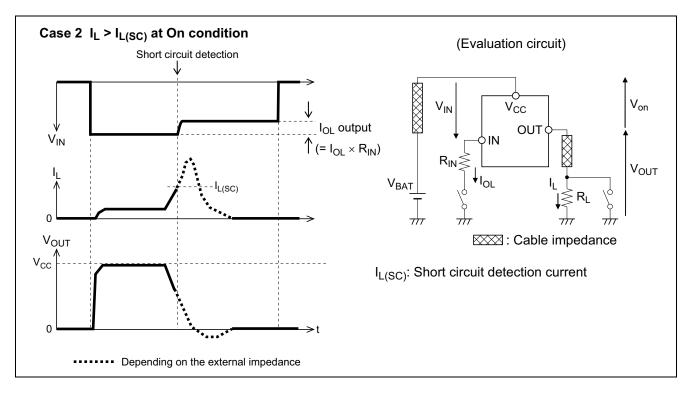




Short Circuit Protection



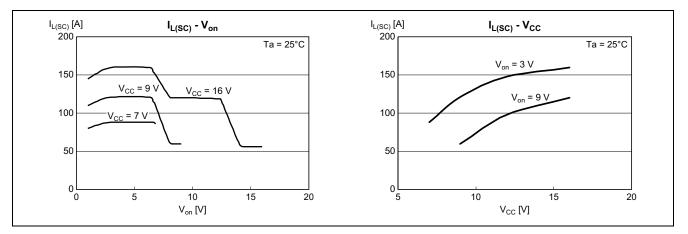
 $\label{eq:Case 2: Short circuit during On-condition. The device shut down automatically when I_L > I_{L(SC)} condition is detected. \\ Then the V_{IN} value is increased by the output of I_{OL} current. Shutdown is latched until the next reset via IN input.$





Typical Short Circuit Detection Current Characteristics

The short circuit detection current changes according V_{CC} voltage and Von voltage for the purpose of to be strength of the robustness under short circuit condition.

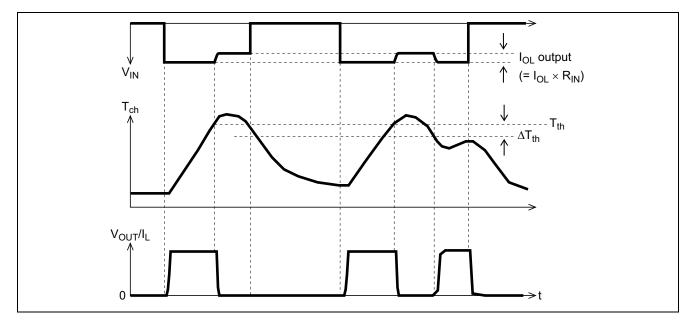


Driving Capability

The μ PD166019 can drive above 215 m Ω , equivalent with 65 W bulb as load resistibility include load itself, wire harness, contact resistance of connector, wiring resistibility of PCB at V_{CC} = 9 to 16 V, T_{ch} = 25°C condition.

Over-Temperature Protection

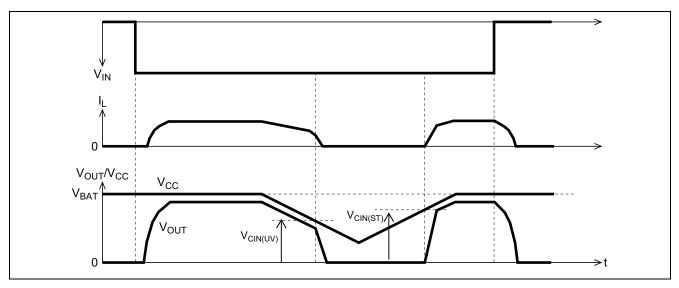
The output is switched off if over-temperature is detected. The device switches on again after it cools down.





Device Behavior at Low Voltage Condition

If the supply voltage, V_{CC} goes down under $V_{CIN(UV)}$, The device shuts down the output. If the supply voltage, V_{CC} increase over $V_{CIN(ST)}$, the device turns on the output automatically. The device keeps off-state if supply voltage, V_{CC} does not increase over $V_{CIN(ST)}$ after under voltage shutdown.



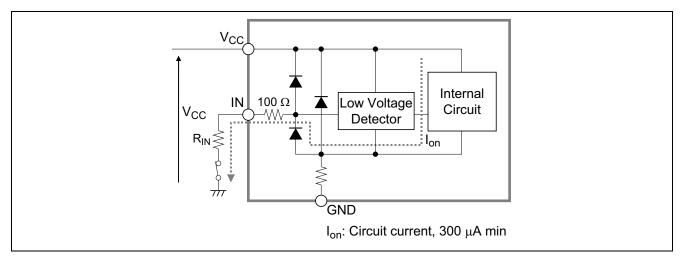
Loss of Ground Protection

In case of complete loss of the device ground connection, but connected load ground, the device securely changes to off-state by low voltage detector. In loss of ground condition voltage of internal circuit is as below. Thus RIN should be bigger than 40 k Ω to activate this function.

Internal circuit voltage = $V_{CC} - I_{on} \times (R_{IN} + 100 \Omega) - Vf$

Low voltage detection voltage: V_{CIN(UV)}

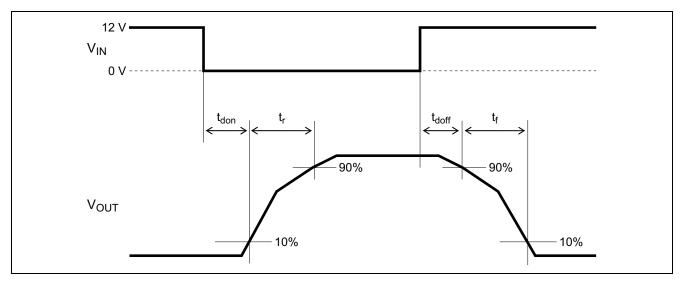
Even though there is possibility to keep on state if R_{IN} is lower than 40 k Ω , output MOSFET is secured in full on state by circuit design.





Measurement Condition

Switching waveform of OUT pin

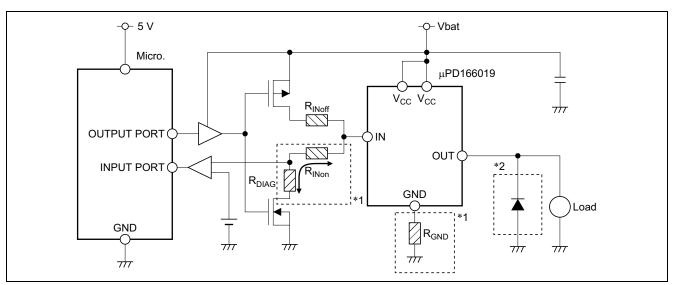


Truth Table

Input Voltage	State	Output	Output Current in Fault Condition
$V_{\text{IN}} \leq V_{\text{INoff}}$	OFF state	OFF	—
$V_{\text{IN}} \geq V_{\text{INon}}$	Normal operation	ON	—
	Over-temperature	OFF (Shut down), restart by $T_{ch} < T_{th} - \Delta T_{th}$	I _{OL}
	Short circuit	OFF (Shut down)	I _{OL}



Application Example in Principle



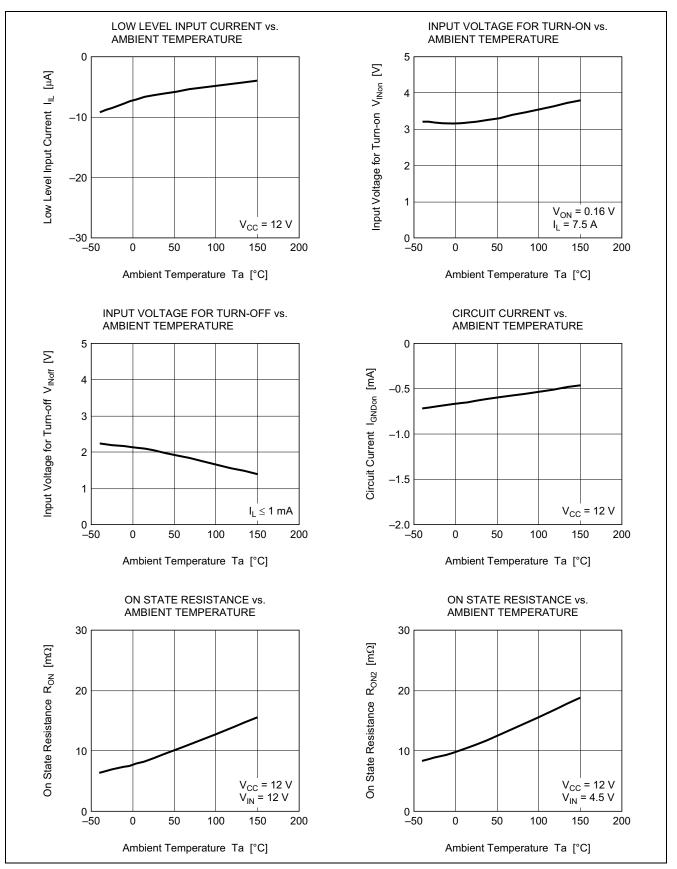
Notes: *1 In case of necessity to eliminate the destruction by the soldering bridge to Vcc: Pin#5, insert a resistor R_{GND} (recommended 1 k Ω max) between GND pin and system ground. In this condition current flows out to IN pin. R_{INon} is recommended 2 k Ω min.

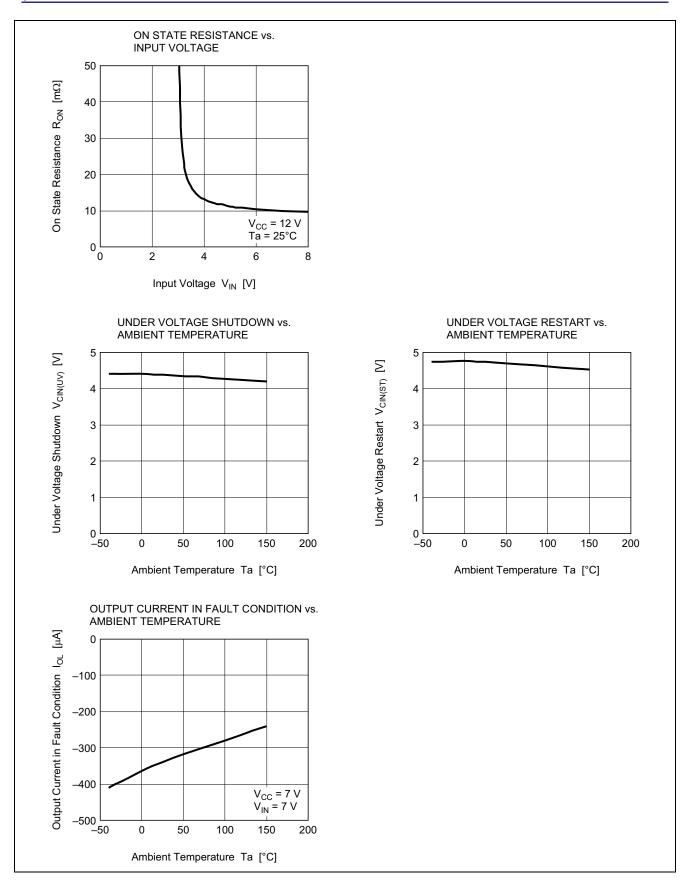
GND to IN current = (R_{GND} × 1 mA – 0.6 V) / R_{INon}

*2 If the load is inductive, it is recommended to connect a free-wheel diode between OUT pin and ground.



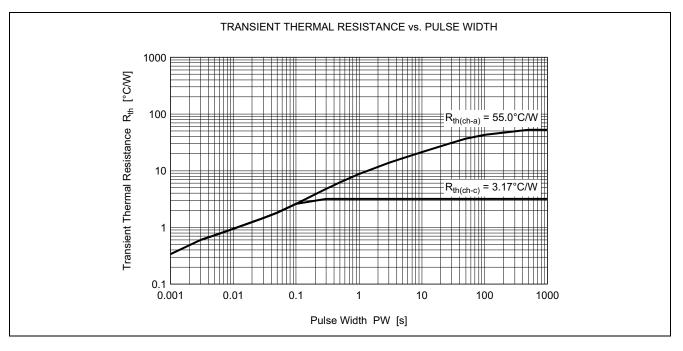
Typical Characteristics



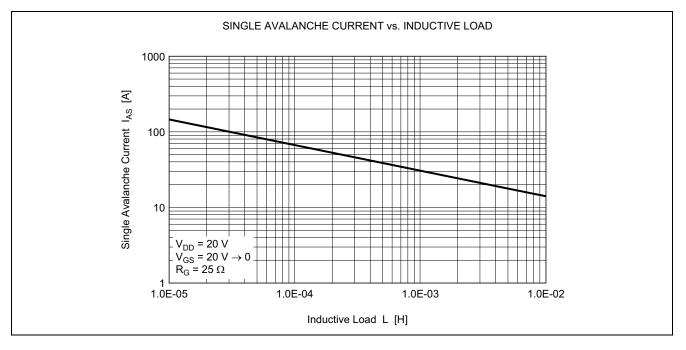




Thermal Characteristics



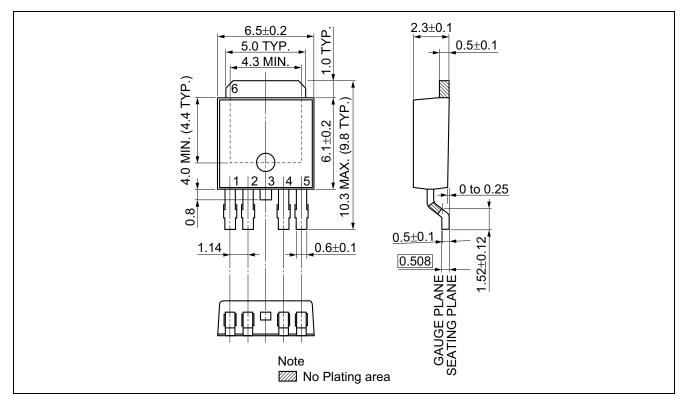
Single Avalanche Current vs. Inductive Load





Package Dimensions (Unit: mm)

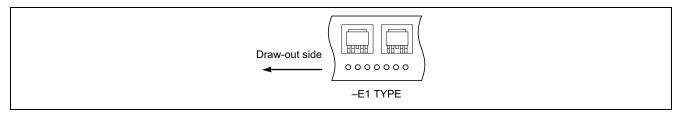
5-pin TO-252 (MP-3ZK)





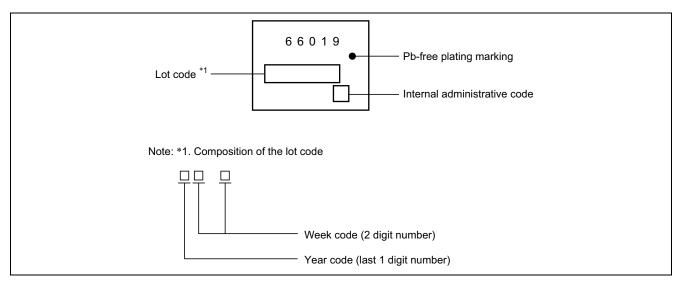
Taping Information

This is one type (E1) of direction of the device in the career tape.



Marking Information

This figure indicates the marking items and arrangement. However, details of the letterform, the size and the position aren't indicated.





Revision Histo	ry
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μPD166019T1F Data Sheet

		Description					
Rev.	Date	Page	Summary				
1.00	Apr 25, 2012	—	First Edition Issued				

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